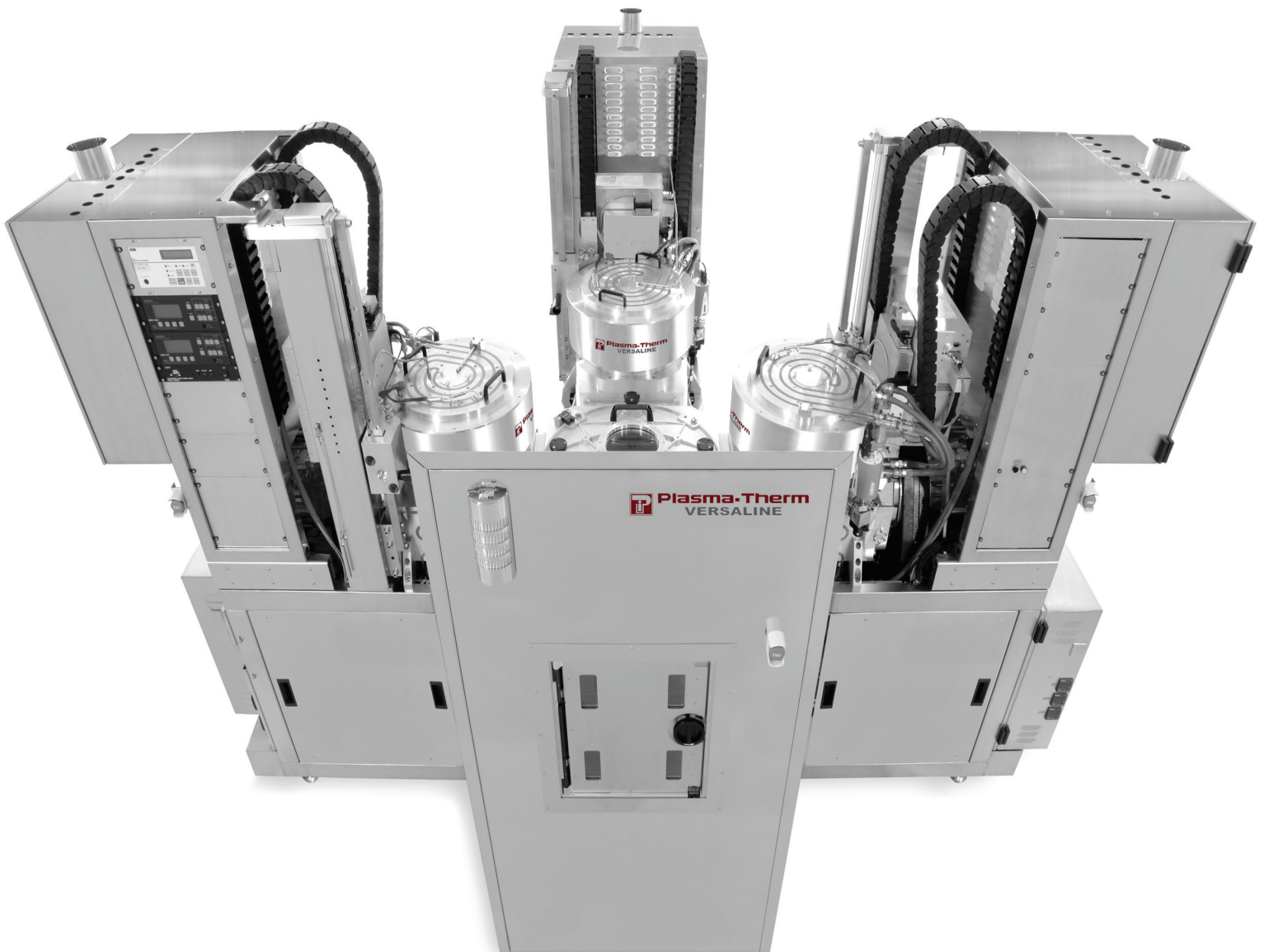


VERSALINE® ETCH

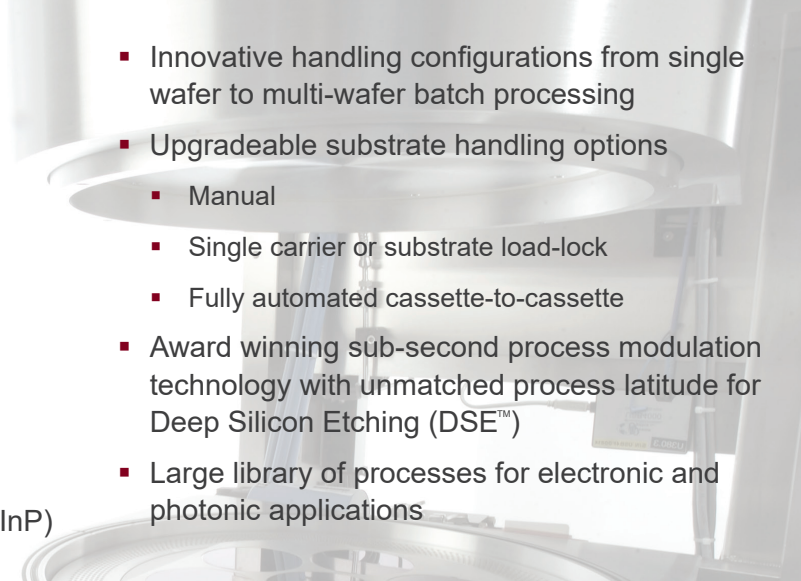
The VERSALINE® platform's modularity facilitates upgradability from R&D through production



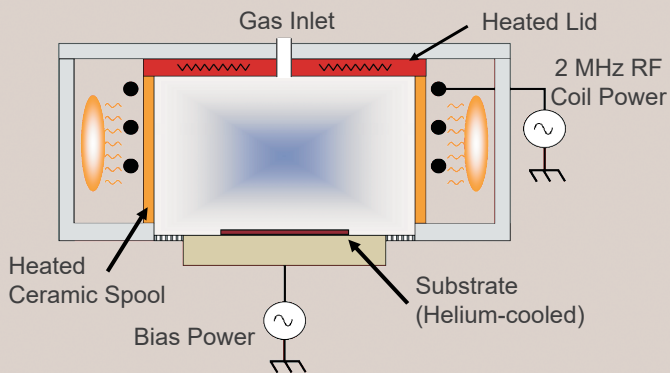
VERSALINE Sets Performance and Flexibility Standards for Rapidly Changing Specialty Markets from R&D through High Volume Production

The VERSALINE platform provides a choice of upgradeable configurations ranging from manual to fully automated loading. The process flexibility for specific applications and requirements can be achieved through technologies such as RIE, DRIE, ICP and Pulsed High Frequency RIE.

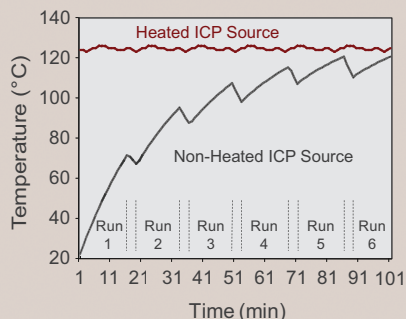
- Maximized productivity and low cost of ownership
 - High uptime (>90%)
 - Low maintenance with heated source
 - Advanced system controls and software
 - Minimize cleanroom costs with small footprint
 - Fast maintenance with easy access
- Clean 2 MHz to optimize performance and reduce capacitive coupling without Faraday shield
- High rate, pillar-free III-V via etching (GaAs, InP)
- Innovative handling configurations from single wafer to multi-wafer batch processing
- Upgradeable substrate handling options
 - Manual
 - Single carrier or substrate load-lock
 - Fully automated cassette-to-cassette
- Award winning sub-second process modulation technology with unmatched process latitude for Deep Silicon Etching (DSE™)
- Large library of processes for electronic and photonic applications



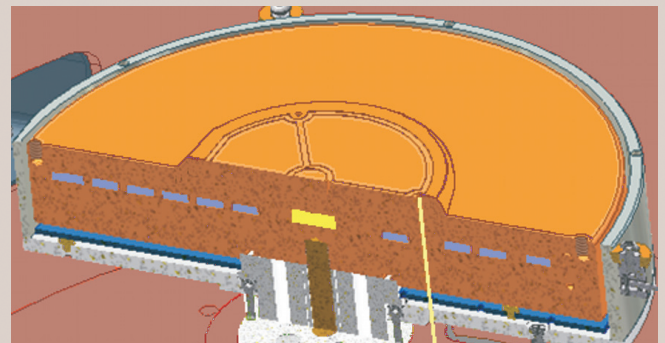
Maximum System Stability and Process Repeatability with Optional Heated ICP



- Eliminate first wafer effects
- Wafer-to-wafer repeatability
- Minimize wet clean cycles
- Extend life of ceramic spool

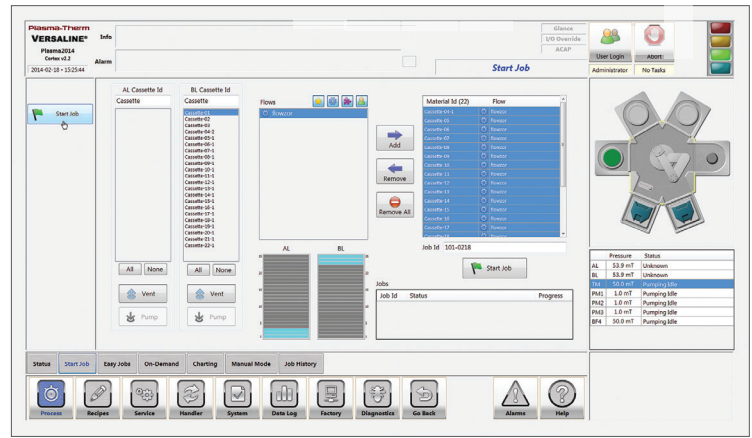


Efficient Substrate Thermal Management



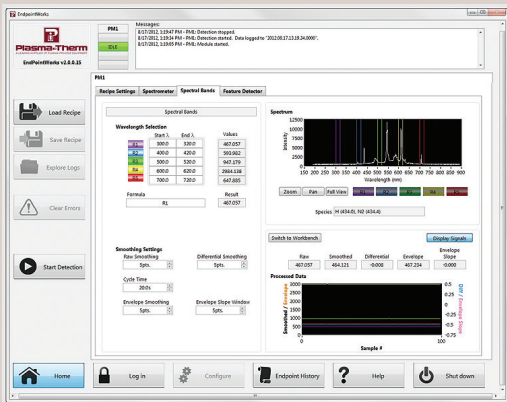
- Monoblock electrode efficiently provides wafer cooling during high rate processes
- Continuous perimeter mechanical clamp for improved helium sealing
- Optimal thermal performance with optional electrostatic chuck (ESC)
- Gentle 3-pin lifting
- Accommodates various substrate thicknesses

- Demonstrated robust software on installed production systems
 - User-friendly control software
 - Comprehensive data logging
 - Automated clean programming
 - Real-time process data display
 - Fully integrated with endpoint system
 - Factory automation compatible (SECS/GEM)
 - Edit recipes during runs
 - Multiple user access levels
 - Alarm history



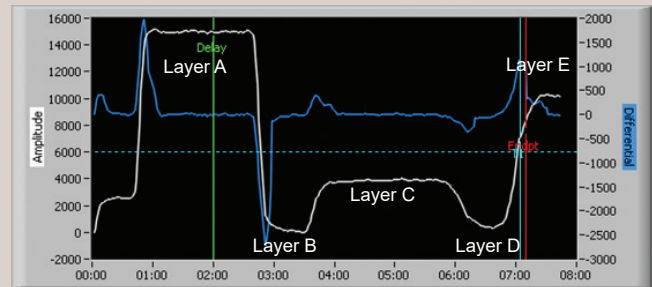
Cortex® Control System

- Advanced process control using Plasma-Therm's unique EndpointWorks®
- Capable of various endpoint inputs
 - Laser Interferometry (LES)
 - Optical Emission Spectroscopy (OES)
 - Optical Emission Interferometry (OEI)
 - System parameters
 - Custom inputs

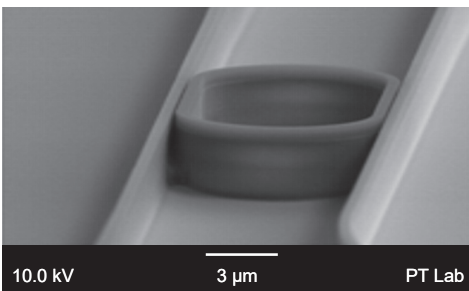


EndpointWorks® Graphical User Interface

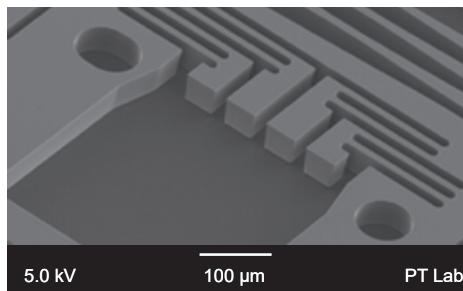
- Optical Emission Spectroscopy (OES)
 - Capable of endpointing on multi-layer stacks
 - Process control of target etch depth
 - Enhanced run-to-run repeatability
- Available for a wide range of process applications



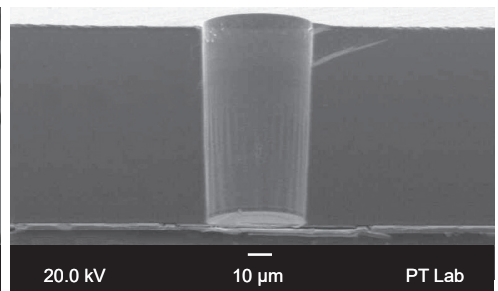
OES Trace of Multi-Layer Etching



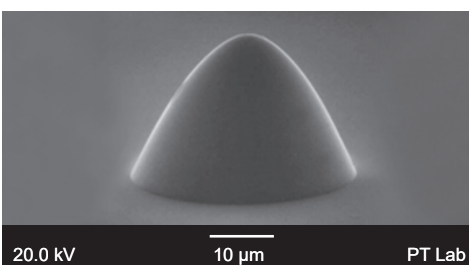
InP Ring Oscillator



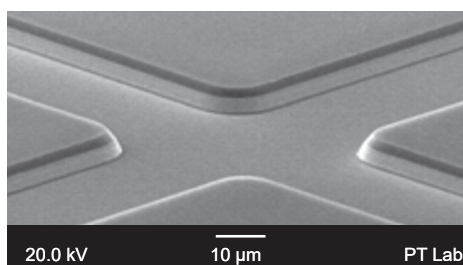
MEMS Micro-Actuator



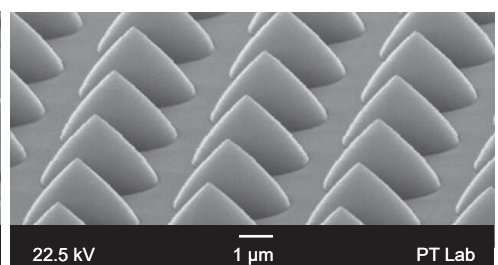
GaAs-Via



Aspherical Quartz Micro-Lens

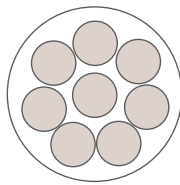


GaN LED

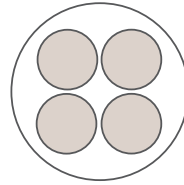


Patterned Sapphire Substrate (PSS)

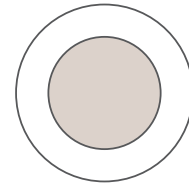
FLEXIBLE SUBSTRATE LOADING CONFIGURATIONS



8 x 2"



4 x 3"



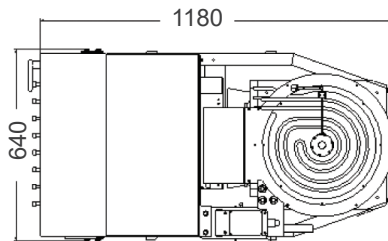
Single 3" to 8"

SPECIFICATIONS

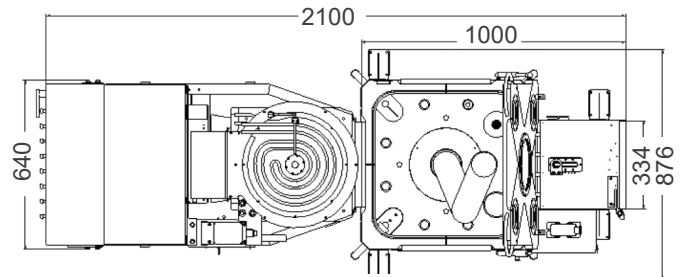
Processing Temperatures	-40°C to 40°C 10°C to 60°C 10°C to 200°C
Electrode Size	8" (200 mm) diameter
Loading	Manual, load-lock or cassette
Control System	Cortex® (with data logging)
Pumping	Dry pump
Gas Lines	Up to 8 channels
Endpoint Detection (with EndpointWorks™ Control System)	Optical Emission Interferometry (OEI) Optical Emission Spectroscopy (OES) Laser Endpoint System (LES)
RF Power Supply	2 kW @ 2 MHz ICP 3.5 kW @ 2 MHz ICP Dual range 600 W @ 13.56 MHz
Installation Configuration	Through-wall or Ballroom
Power Requirements	200-230 V, 50/60 Hz
Certifications	CE, SEMI-S2, S8



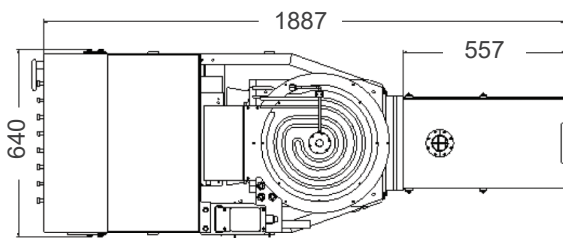
FLEXIBLE HANDLER CONFIGURATIONS



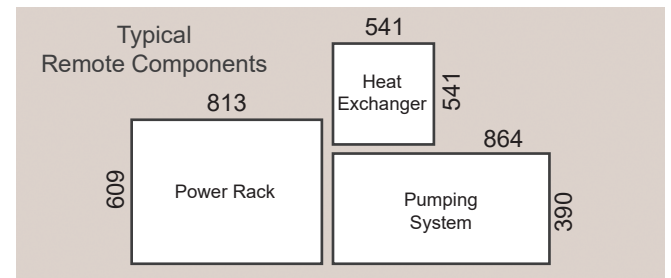
Single Wafer/Batch Manual Loading (RIE only)



Cassette-to-Cassette/Load-lock



Single Wafer/Load-lock



Units: mm



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